



Atty. Dkt. No. 039153-0649 (H0982)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Xiang et al.

Title: FULLY DEPLETED STRAINED  
SEMICONDUCTOR ON  
INSULATOR TRANSISTOR  
AND METHOD OF MAKING  
THE SAME

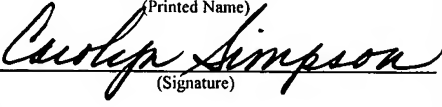
Appl. No.: 10/773,026

Filing Date: 02/05/2004

Examiner: Doty, Heather Anne

Art Unit: 2813

Conf. No.: 6093

<b>CERTIFICATE OF EXPRESS MAILING</b>	
I hereby certify that this correspondence is being deposited with the United States Postal Service's "Express Mail Post Office To Addressee" service under 37 C.F.R. § 1.10 on the date indicated below and is addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.	
EV 828711679 US	11/13/07
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Carolyn Simpson	
(Printed Name)	
	
(Signature)	

**ISSUE FEE TRANSMITTAL**

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Enclosed herewith please find Issue Fee Transmittal Form PTOL-85(B).

**COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE**

The Examiner stated the following:

Claims 28-31 are allowed for the reasons indicated in the Office action dated 3/2/2007.

Regarding claims 11, 12, and 14-27, prior art does not teach or suggest, in combination with the other claimed limitations, providing an aperture in the semiconductor/germanium layer for each of the plurality of transistors. Cheng et al., the closest prior art of record, teaches forming a device layer on top of the semiconductor/germanium layer. It would therefore not be reasonable to combine other relevant prior art with the teachings of Cheng et al. to arrive at the invention as claimed by Applicant.

While the Applicants agree that the allowed claims recite a combination of subject matter that is patentable over the cited references, the Applicants do not necessarily agree with or acquiesce in the statement of reasons for allowance given by the Examiner. Moreover, the Applicants note that the recited subject matter as well as various other subject matter and/or combinations of subject matter may be patentable for other reasons than those given by the Examiner. The Applicants expressly reserve the right to set forth additional and/or alternative reasons for patentability and/or allowance in connection with the present Application or in any other future proceeding.

### REMARKS

These Comments on Statement of Reasons for Allowance under 37 C.F.R. § 1.104 are intended to be responsive to the Notice of Allowance dated August 14, 2007.

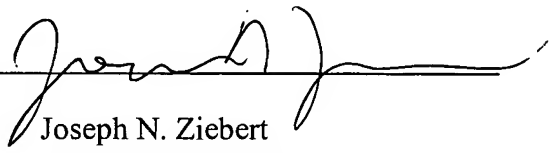
A credit card payment form in the amount of \$1,440.00 for payment of the Issue Fee is enclosed.

The Commissioner is hereby authorized to charge any additional fees which may be required regarding this application under 37 C.F.R. §§ 1.16-1.17, or credit any overpayment, to Deposit Account No. 19-0741. Should no proper payment be enclosed herewith, as by the credit card payment form being unsigned, providing incorrect information resulting in a rejected credit card transaction, or even entirely missing, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 19-0741.

Respectfully submitted,

Date 11-13-07

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